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	APPLICANT WAGNER et al.	
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U. S. PATENT DOCUMENTS

EXAMINER INITIAL	PATENT NUMBER	PATENT DATE	NAME	CLASS	SUBCLASS	FILING DATE
CY	5,247,190	September 21, 1993	Friend et al.			
	5,703,436	December 30, 1997	Forrest et al.			
	5,707,745	January 13, 1998	Forrest et al.			
	5,834,893	November 10, 1998	Bulovic et al.			
	5,844,363	December 1, 1998	Gu et al.			
	6,013,982	January 11, 2000	Thompson et al.			
	6,087,196	July 11, 2000	Sturm et al.			
	6,091,195	July 18, 2000	Forrest et al.			
	6,294,398	September 25, 2001	Kim et al.			
	6,303,238	October 16, 2001	Thompson et al.			
	6,337,102	January 8, 2002	Forrest et al.			
	6,468,819	October 22, 2002	Kim et al.			
	6,602,540	August 5, 2003	Gu et al.			
CY	2002/0134984	September 26, 2002	Igarashi			

FOREIGN PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO

OTHER DOCUMENTS

EXAMINER INITIAL		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
CY		Shtein et al., U.S. Patent Application Serial No. 10/233,470, filed September 4, 2002, entitled "Process and Apparatus for Organic Vapor Jet Deposition".
CY		Hsu et al., "Plastic Deformation of Thin Foil Substrates with Amorphous Silicon Islands into Spherical Shapes," <u>Mat. Res. Soc. Symp. Proc.</u> , Vol. 621 (2000)
CY		Hsu et al., "Amorphous Si TFTs on plastically-deformed substrates with 3-D shapes", Tech. Dig. Of the 59 th Device Research Conference, June 25-27, 2001, University of Notre Dame, Notre Dame, Indiana.
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EXAMINER INITIAL		AUTHOR, TITLE, DATE, PERTINENT PAGES, ETC.
C		Hsu, et al., "Thin-film transistor circuits on large-area spherical surfaces," <u>Applied Physics Letters</u> , vol. 81, no. 9, pp. 1723-1725 (August 26, 2002).
		R. Bhattacharya et al., "Island Edge Coverage by Metal Interconnects for Three Dimensional Circuits".
		Hsu et al., "Spherical deformation of compliant substrates with semiconductor device islands," <u>Journal of Applied Physics</u> , vol. 95, no. 2, pp. 705-712 (January 15, 2004).
		Hsu et al., "Effects of Mechanical Strain on TFTs on Spherical Domes," <u>IEEE Transactions on Electron Devices</u> , vol. 51, no. 3, pp. 371-377 (March 2004).
C		Wang et al., "Curved Silicon Electronics," <u>Mat. Res. Soc. Symp. Proc.</u> , Vol. 769 (2003).
		"Interconnecting Indium Tin Oxide Islands on a Spherical PET Surface", Symposium I Flexible Electronics - Materials and Device Technology, <u>Mat. Res. Soc. Symp.</u> , April 12-16 2004, publicly available online 3/22/04 as volume 814.
		Sturm et al., "Three-Dimensional Electronic Surfaces," <u>Mat. Res. Soc. Symp. Proc.</u> , Vol. 636 (2001).
		Brochure of "Southwall Altair O Transparent Conductive Film," December 1995, 2 pages, Southwall Technologies, Inc., Palo Alto, CA.
C		Z. Suo, et al., "Mechanics of rollable and foldable film-on-foil electronics", <u>Applied Physics Letters</u> , Volume 74, Number 8, pp. 1177-1179, February 22, 1999.

EXAMINER	<i>Calmon Lai</i>	DATE CONSIDERED	4.6.05
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